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2 Feb  
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#5

In re application : John V. Reynders, et al.  
Application No. : 10/027,886  
Filed : October 19, 2001  
For : EFFICIENT SYSTEM AND METHOD OF NODE AND  
LINK INSERTION FOR DEADLOCK-FREE ROUTING  
ON ARBITRARY  
Attorney's Docket : SUNM-058XX

Group Art Unit: 2181

\*\*\*\*\*  
I hereby certify that this correspondence is being deposited with  
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Patent Examination, Customer Service Center, Washington,  
D.C. 20231 on 3/5/03.

By: 

Victor B. Lebovici  
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Attorney for Applicants

\*\*\*\*\*

L E T T E R

Commissioner for Patents  
Office of Initial Patent Examination  
Customer Service Center  
Washington, D.C. 20231

Sir:

In reviewing the Filing Receipt (copy enclosed) for the  
above-identified patent application, the following error was  
noted:

Application No.: 10/027,886  
Filed: October 19, 2001  
Group Art Unit: 2181

Title

Efficient system and method of node and link insertion for  
deadlock-free routing on **arbitrary** topologies

should read:

Title

Efficient system and method of node and link insertion for  
deadlock-free routing on **arbitrary** topologies

Kindly correct the error of the Patent and Trademark Office  
and issue a corrected Filing Receipt.

Respectfully submitted,

JOHN V. REYNDERS, ET AL.

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Enclosure  
287254



## UNITED STATES PATENT AND TRADEMARK OFFICE

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WASHINGTON, D.C. 20231  
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/027,886	10/19/2001	2181	1196	P6270	10	22	8

SUNM-05FRX

CONFIRMATION NO. 3221

207

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## FILING RECEIPT



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Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

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## Assignment For Published Patent Application

Sun Microsystems, Inc.;

## Domestic Priority data as claimed by applicant

## Foreign Applications

If Required, Foreign Filing License Granted 02/05/2002

Projected Publication Date: 04/24/2003

Non-Publication Request: No

Early Publication Request: No

## Title

Efficient system and method of node and link insertion for deadlock-free routing on arbitrary

arbitrary!

S.  
4/17/03  
d.

topologies

**Preliminary Class**

710

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